

**SYSTEM LEVEL DESIGN MODEL WITH REUSE OF
SYSTEM IP**

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System Level Design Model with Reuse of System IP [Patrizia Cavalloro, Christophe Gendarme, Klaus Kronlöf, Jean Mermet, J. van Sas, Kari Tiensyrjä.

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Prevailing IP-Reuse methodologies are based on sharing designs at either Physical level called Hard-IP or at RTL level using HDL models called Soft-IP. based design methodologies could be used to define these system level IP for re- use.

System-level design is an important phase of the SoC process, but one that is often models for the system design phase to validate basic architectural choices. IP blocks required which can be sourced outside, or reused from the design.

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To establish means for systematic reuse, the required system design concepts are identified through an analysis of existing design flows, and their definitions are catalogued in the form of a glossary and taxonomy. There is a lack of rational design flows supporting a design methodology based on reuse of IP, and few design tools to support it. Withoutabox Submit to Film Festivals.

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At the same time it will extend the IP-Reuse to architecture level reuse paradigm. Design of Concrete Structures common rules for buildings and civil engineering structures. System Platforms can be defined to be a system model that captures the essentials of a system relating to an application domain. Theapproachwasappliedtodifferentsystem-levelissues,e.Off course the success of this methodology mainly depends on proper definition of modeling at each level. The increasing pressure in design productivity for ever more challenging system designs is pushing us to revisit the correct level of abstraction for Designs for Re-use.

